

1200V thinQ!TM SiC Schottky Diode

Features:

- Revolutionary Semiconductor Material -Silicon Carbide
- Switching Behaviour Benchmark
- No Reverse Recovery / No Forward Recovery
- Temperature Independent Switching Behaviour
- Qualified According to JEDEC¹⁾ Based on **Target Applications**

Applications:

- Motor Drives / Solar Inverters
- High Voltage CCM PFC
- Switch Mode Power Supplies
- High Voltage Multipliers



Chip Type	V_{BR}	I _F	Die Size	Package		
IDC05S120	1200V	5A	1.692 x 1.692 mm ²	sawn on foil		

MECHANICAL PARAMETERS					
Raster size	1.692 x 1.692				
Anode pad size	1.156 x 1.156	mm ²			
Area total / active	2.86 / 2	7			
Thickness	362	μm			
Wafer size	75	mm			
Flat position	0	deg			
Max. possible chips per wafer	1321 pcs				
Passivation frontside	Photoimide				
Pad metal 3200 nm Al					
Backside metal	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond Electrically conductive glue or solde					
Wire bond AI, ≤ 350µm					
Reject ink dot size	Ø ≥ 0.3 mm				
Recommended storage environment	Store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				

< 6 month at an ambient temperature of 23°C



ELECTRICAL PARAMETERS

Maximum Ratings

Parameter	Symbol	Condition	Value	Unit	
Repetitive peak reverse voltage	V_{RRM}	<i>T</i> _j =25 °C	1200	V	
DC blocking voltage	V_{DC}		1200	- V	
Continuous forward current, limited by T _{jmax}	I _F		5		
Surge non repetitive forward current,	,	$T_C = 25^{\circ}C$, $t_P = 10 \text{ ms}$	29	_	
sine halfwave	I _{F,SM}	$T_C = 150^{\circ} C$, $t_P = 10 \text{ ms}$	25	A	
Repetitive peak forward current, limited by thermal resistance R _{th}	I _{F,RM}	$T_C = 100$ °C, $T_j = 150$ °C, $D = 0.1$	23		
Non-repetitive peak forward current	$I_{F,max}$	$T_C = 25^{\circ}C$, $t_P = 10\mu$ s	110		
i ² t value	$\int i^2 dt$	$T_C = 25^{\circ}C, t_P = 10 \text{ ms}$	4	- A ² s	
i i value	Jiai	$T_C = 150^{\circ} C$, $t_P = 10 \text{ ms}$	3] ^ 5	
Operating junction and storage temperature range	$T_{\rm j}$, $T_{ m stg}$		-55+175	°C	

Static Characteristics (tested on wafer)

Parameter	Symbol	Conditions		Unit		
raiailletei	Syllibol		max.			
Reverse current	I _R	V _R =1200V, T _j =25°C		5	120	μA
Diode forward voltage	V _F	I _F =5A, T _j =25°C		1.6	1.8	V

Static Characteristics (not subject to production test - verified by design / characterization)

Parameter	Symbol	Conditions		Unit		
raiailletei	Syllibol				max.	
Reverse current	I_{R}	V _R =1200V, T _j =150°C		20	1000	μΑ
Diode forward voltage	V _F	I _F =5A, T _j =150°C		2.5	3	V



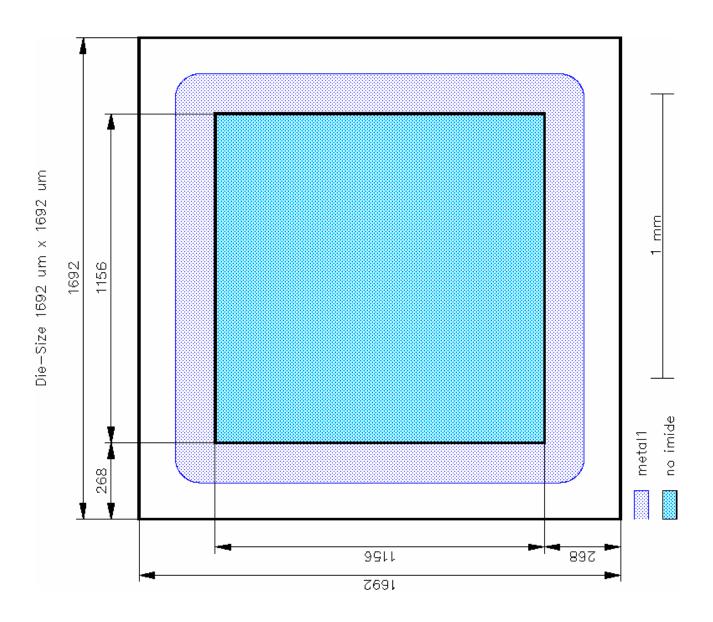
Dynamic Characteristics (not subject to production test - verified by design / characterization)

Parameter	Symbol	Conditions		Value			Unit
- arameter	Syllibol			min.	Тур.	max.	
Total capacitive charge ³⁾	Q_C	di/dt=200A/ms	T _j =150 °C		18		nC
Switching time ²⁾	t_c		T _j =150 °C			<10	ns
			V _R = 1 V		250		
Total capacitance	С	f=1MHz	V _R =300V		20		pF
			V _R =600V		18		

 $^{^{1)}}$ J-STD20 and JESD22 $^{2)}$ t_{c} is the time constant for the capacitive displacement current waveform (independent from $T_{j},\ l_{LOAD}$ and di/dt), different from $t_{rr},$ which is dependent on $T_{j},\ l_{LOAD},$ di/dt. No reverse recovery time constant t_{rr} due to absence of minority carrier inject. $^{3)}$ Only capacitive charge occurring, guaranteed by design (independent from $T_{j},\ l_{LOAD}$ and di/dt).



CHIP DRAWING





FURTHER ELECTRICAL CHARACTERISTICS

This chip data sheet refers to the device data sheet

INFINEON TECHNOLOGIES

IDH05S120

Description:

AQL 0,65 for visual inspection according to failure catalogue

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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